

Inventor: McCrackin, et al.
Docket No.: 14233.0008US01
Title: TIMER RAMP-UP CIRCUIT AND METHOD FOR A SOUND MASKING SYSTEM
Serial No.: 10/634,228
Sheet 1 of 3

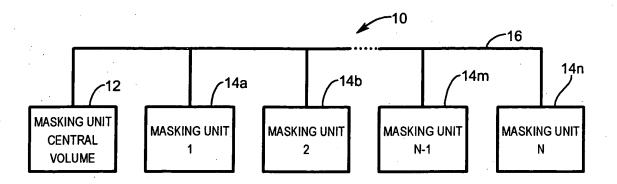


FIG. 1

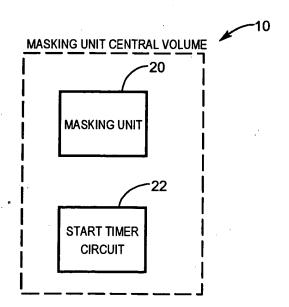


FIG. 2

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Sheet 2 of 3 OUTPUT OUT1 ALL RESISTORS ARE 1/8 WATT, 5% TOLERANCE. ALL CAPACITORS HAVE MINIMUM WV OF 6V UNLESS OTHERWISE SPECIFIED J1 IS A WIRE JUMPER. LM358N^{+12V} * % 1.2M 1.2M 1.2M B OUT 328 304 330 GAIN = 2.4A OUT ₹\$\$ \$ ATTENUATION IS 4.8:1 340-2.2M 0.1uF 8 332 338 ∑\$<u>~</u> Ŋ PWM_OUT ISPCLOCK **ISPDATA** _C3 _0.1uF@16V 9 8 311 0.1⊾F CSP 촹 S 72 REFOUT REFOUT V ss **AN78L05 U1 GPO/SPDAT** GP4/CLKOUT GP1/ISPCLK GP2/INT AN78L12 302 Z GP3/MCLRVpp .0.33uF@50V 2 GP5/CLKIN PIC12F675 <u>×</u> \overline{c} 314, +2\ GND1 RESET *****

Inventor: McCrackin, et al. Docket No.: 14233.0008US01 Title: TIMER RAMP-UP CIRCUIT AND METHOD FOR A SOUND MASKING SYSTEM Serial No.: 10/634,228 Sheet 3 of 3 JAN 05 2004 400 **FACTORY START TEST MODE** 402 403 Yes No JUMPER PRESENT? 405 SAMPLE 404 Yes RESET ANALOG INPUT BLU No 409 **INTERNAL VOLUME** SETTING= SET ANALOG 406 **READ LAST INTERNAL** START_MILLIV **OUTPUT TO VOLUME SETTING IN** LEVEL **MEMORY** 407 408 STORE INTERNAL VOLUME No TEST RANGE LEVEL IN NONVOLATILE **MEMORY COMPLETE SAMPLE ANALOG** Yes **INPUT BLU EXIT FACTORY TEST** MODE **GENERATE LEVEL FOR ANALOG OUTPUT TO SOUND MASKING UNIT** 415 **USING INTERNAL VOLUME LEVEL** AND ANALOG INPUT LEVEL BLU 410 417 PHYSICALLY REMOVE **SET INTERNAL** JUMPER (308) - FIG. 3 **VOLUME SETTING** 419 No INTERNAL VOLUME LEVEL =FINAL-MILLIV **FIG. 4** Yes RETURN; RAMP-UP VOLUME OPERATION COMPLETE